SD NAND 6

/

(4) AcMD6 / GO IDLE (CMDO) 1

:

a) “ ”

b) ACMD6

2GB

2GByte (READ BL LEN- write BL LEN) 1024 CMD16 512 512 (2GByte)

(1 4)

(off-card ECC is used)

endl (1 4 HIGH).

1 4 CRC

SD SDl

BLOCK_LEN_ERROR ADDRESS ERROR

512

512

CMD16 512 READ_BL_LEN CRC

CMD17 (READ_SINGLE_BLOCK)

CMD18 (READ_MULTIPLE_BLOCK)

(CMD12) l stop

stop

CMD18

OUT_OF_RANGE

ADDRESS_ERROR

Data State

(

i.e.ADDRESS_ERROR) TRAN

*1: “ ” CMD16 value 512 (Misalign Partial)

*2: Blocklen 512 512 CRC “ ADDRESS_ERROR ” CMD12

CRC 1 4 CRC BLOCK_LEN_ERROR ADDRESS_ERROR

(CMD24 - 27,42,56 (w))

1 4 CRC WRITE_BL_LEN 1k 2k CMD16 block Length 512 (WRITE_BL_PARTIAL = 0)

*1: “ ” CMD16 value 512 (Misalign Partial) “ Blocklen ”

*2: Blocklen “ BLOcK_LEN_ERROR ”

*3: “ ADDRESS_ERROR ”

WRITE_BL_PARTIAL(=1)

(CSD WRITE_BLK_MISALIGN)

ADDRESS_ERROR
Receive-data-State

(
i.e.ADDRESS_ERROR) tran
WP_VIOLATION

CSD CRC CSD
ROM

CRC
WRITE_BLOCK
DATO SEND_STATUS (CMD13)
READY_FOR_DATA

CMD7( )

DAT

CMD DATO-3 ( )

杭州瀚海微科技有限公司
www.hanhai-tech.com